AMENDMENT UNDER 37 C.F.R. 1.111
Serial Number: 09/470,329
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Title: PREVENTION OF LIVE-LOCK IN A MULTI-PROCESSOR SYSTEM

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IN THE DRAWINGS

The drawings are objected to under 37 CFR 1.83(a).

Applicant resepctfully traverses the objection of these drawings under 37 CFR 1.83(a).

Referring to Figures 4A and 4B, it can be seen that steps 410 and 414 of Figure 4A, as well as steps 410, 414 and 416 of Figure 4B actually show and support the feature "retrying the first bus transaction and each subsequent non modifying bus transaction for the shared resource until the status bit is clear" as recited in claim 1. Claims 3-6 recite ways to clear the status bit. Referring again to Figures 4A and 4B, "CLEAR STATUS BIT" is shown in step 412 of Figures 4A, as well as steps 412 and 418 of Figure 4B. In specification, page 7, lines 13-18 and page 8, lines 26-31 describe in detail how to clear the status bit. The discussion for drawings with respect to claims 1 and 3-6 also apply to drawings with respect to claims 7-9. Therefore, Applicant respectfully submits that the drawings of the present Application meet the requirement of 37 CFR 1.83(a).

Applicant respectfully requests reconsideration and withdrawal of the objection of the drawings under 37 CFR 1.83(a).